



Mapping

Nested model 
$$PRAM \left( \begin{smallmatrix} T = \# & of & steps \\ p = \# & of & processors \end{smallmatrix} \right)$$
  
 $W, S$   $max \left( \begin{smallmatrix} W \\ p \end{smallmatrix} , S \end{smallmatrix} \right) \leq T \leq \begin{smallmatrix} W \\ p \\ p \end{smallmatrix} + S$   
 $We & want this to dominate ... this happens if  $P \leq \begin{smallmatrix} W \\ S \\ parallelism = \begin{smallmatrix} W \\ S \\ S \\ \end{bmatrix}$$ 

Proof for greedy scheduling theorem

Def: node is at level & if its longest path to root is l.

Lemma: on every step, either: 1. put down p pebbels 2. finish a level

> <u>Proof</u> AFSOC let Lj be longest level that all vodes are covered Then at Lj+1 all vodes are either done or ready. Then if we put less than p petbels and not finish the level, we're not greedy

## # Array Sequences, bottom up

Data structure : array ( other impl could use list, function, trees, ...)

Primeives for array

W 2 get i-th elem acij OLD DUD lal get length 001) OLI) alloc (n) allocate array of length n O(I) an panallel For (pFor) i = x to y, evaluate e(i) in panallel ZW(e(1)+1 Nax S(e(i)) I Has manoridable side effect

Race condition : both write or one read one write I Avoid this

Implementations

```
map f A =

R = alloe |A|

pFor i = 0..(|A|-1)

R[i] = f A[i]

ret R

tabulate f n =

R = alloe n

pFor i = 0..(n-1)

R[i] = f i

ret R
```