Lec 25 Concurrent Data Structure & Work Stealing Scheduling
Key ideas - Lock-free data structure - Linearisation - Compare and swap (CAS) - Concurrent deque - Randoniused stealing
Recall greedy scheduling $T = \frac{W}{P} + S$ But in real world we need to find work to schedule
Working with async, parallel processors
Model memory Can delay
Assume arbitrary interleaving can bet unscheduled can have different clock rate
PI $r. \leftarrow mem[a]$ P2 $r_2 \leftarrow mem[a]$ race condition possible $r_1 = r_1 + 1$ $r_2 = r_2 + 1$ mem[a] $\leftarrow r_2$ rem[a] $\leftarrow r_2$ rem[a] $\leftarrow r_2$
Lock-free data structure
Def Lock free data structure - Supports certain operations - Shared across processes - At least one process making process (puts "lock-free lock" i.e. some launda around erritical code)
Linearisability
Operactions: load, increment push, pop
> They can appear interteaved but correctness captured sequentially

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# Compare and swap
  On x86: CMPXCHG
 Analogous to:
    CAS: \alpha ref \rightarrow (\alpha \times \alpha) \rightarrow bool
                                                   Note this code is not safe.
    CAS r (old, new) =
       let
                                                   Processor implements this on
         a = !c
                                                   heurolware level as instruction
       in
          if a = old then (r := new; true)
          else false
       end
  Linearisable increment
                             < no lock involved
    Inc (r: int ref) =
       let
        a = !r
       in
         if cas r (a, a+1) then ()
         else Inc r
       end
# Work stealing scheduler ( randomised )
  How to do fllg?
  → Forking puts job into shared data structure
Idle threads find the job and do it
  Each processor keeps a deque DQ
lock-free, linearisable
                                                 pushbot
  - when encountering fild on processor
                                                  P
      DQp pushbot (q)
       run f
      wait for result of g
  · If processor p done or while waiting
```